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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Sorg et al.  
Serial No. : 10/635,937  
Filed : August 5, 2003  
Title : ELECTRICAL LEADFRAMES, SURFACE MOUNTABLE SEMICONDUCTOR  
COMPONENTS, LEADFRAME STRIPS, AND THEIR METHOD OF  
MANUFACTURE

Art Unit : 2822  
Examiner : Kiesha L. Rose

MAIL STOP AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

A copy of the reference listed on the attached form PTO-1449 is enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office Action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date:

Nov. 17, 2004

Jennifer A. Zarosco  
Reg. No. 54,563

Customer No. 26181  
Fish & Richardson P.C.  
Telephone: (650) 839-5070  
Facsimile: (650) 839-5071

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 17, 2004

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Carlos A. Brasil

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12406-031001	Application No. 10/635,937
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Joerg-Erich Sorg et al.	
		Filing Date August 5, 2003	Group Art Unit 2822

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AQ	Fairchild Semiconductor: "Surface Mount LED Lamp, Super Bright 0402", Fairchild Semiconductor Corporation, August 31, 2001, pp. 1-6.
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	